

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1 1. (Currently Amended) A method for obfuscating computer program
2 instructions upon disassembly, the method comprising:
3 inserting an [obfuscating] interrupt instruction for causing a disassembler to not
4 disassemble one or more bytes subsequent to the interrupt [obfuscating] instruction; and
5 inserting a branch instruction to invoke execution of one or more bytes
6 subsequent to the [obfuscating] interrupt instruction.

1 2. (Currently Amended) The method of claim 1 and comprising repeating said
2 inserting an interrupt instruction and said inserting a branch instruction [, wherein two or more of
3 the obfuscating instructions are used adjacently to increase the number of the one or more bytes].

1 3. (Currently Amended) The method of claim 1, wherein said branch instruction
2 is a jump instruction [two or more of the obfuscating instruction is an INT instruction].

1 4. (Currently Amended) A method for obfuscating computer program
2 instructions upon disassembly, the method comprising:
3 inserting an obfuscating instruction for causing a disassembler to not disassemble
4 one or more bytes subsequent to the obfuscating instruction, wherein said obfuscating instruction
5 is an INT instruction; and
6 inserting a branch instruction to invoke execution of one or more bytes
7 subsequent to the obfuscating instruction, said method [The method of claim 3,] including the
8 step of inserting the following code:

9 JMP \$+4

10 INT 35h.

1 5. (Original) The method of claim 1, wherein the steps are performed manually.

1 6. (Original) The method of claim 1, wherein the steps are performed by a
2 software process.

1 7. (Currently Amended) The method of claim 6, wherein parameters are supplied
2 to the software process, the method further comprising supplying a parameter to the software
3 process to specify the frequency with which [that] an [obfuscating] interrupt instruction is to be
4 inserted in a predetermined program.

1 8. (Currently Amended) The method of claim 7, wherein the frequency is
2 specified as a number of instructions of the predetermined program between each insertion of the
3 [obfuscating] interrupt instruction.

1 9. (Currently Amended) A computer-readable media including the following
2 instructions executable by a processor:

3 an [obfuscating] interrupt instruction for causing a disassembler to not
4 disassemble one or more bytes subsequent to the interrupt [obfuscating] instruction; and
5 a branch instruction to invoke execution of one or more bytes subsequent to the
6 [obfuscating] interrupt instruction.

1 10. (Currently Amended) A computer-readable media including the following
2 obfuscating instructions executable by a processor:

3 JMP \$+4

4 INT 35h

1 11. (Currently Amended) A computer-readable media including the following
2 obfuscating instructions executable by a processor:

3 JMP \$+4

4 INT 35h

5 INT 20h

- 1 12. (Currently Amended) An apparatus for obfuscating computer program
2 instructions upon disassembly, the apparatus comprising:
3 an interrupt instruction for causing a disassembler to not disassemble one or more
4 bytes subsequent to the interrupt [obfuscating] instruction; and
5 a branch instruction to invoke execution of one or more bytes subsequent to the
6 [obfuscating] interrupt instruction.